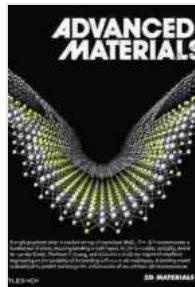


Through Silicon Vias: Materials, Models, Design, and Performance

The Ultimate Guide to Silicon Vias

Through silicon vias (TSVs) are a critical enabling technology for advanced microelectronic packaging. They provide electrical connections between different layers of a semiconductor device, allowing for increased functionality and reduced form factor. Through Silicon Vias: Materials, Models, Design, and Performance is the definitive guide to this vital technology.



Through Silicon Vias: Materials, Models, Design, and Performance

★★★★★ 5 out of 5



This comprehensive book covers everything you need to know about TSVs, from materials selection to design optimization and performance analysis. Written by a team of leading experts in the field, Through Silicon Vias: Materials, Models, Design, and Performance is the essential resource for anyone involved in the design, fabrication, or use of TSVs.

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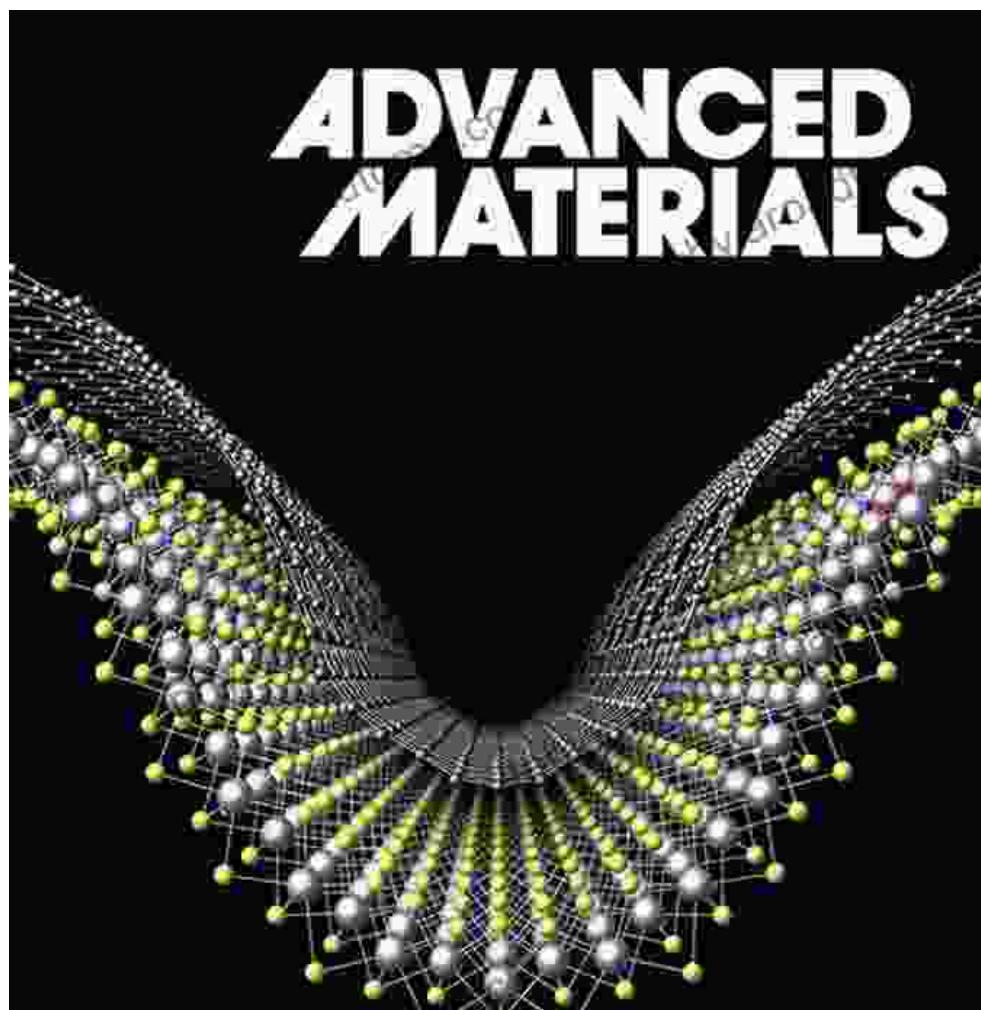
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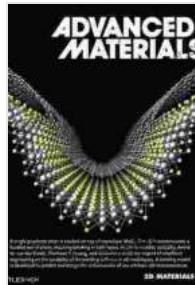


ADVANCED MATERIALS

A single graphene sheet is stacked on top of monolayer MoS₂. The 2D heterostructure is buckled out-of-plane, inducing bending in both layers. In article number 2007369, Arend M. van der Zande, Pinshane Y. Huang, and co-workers study the impact of interfacial engineering on the tunability of the bending stiffness in 2D multilayers. A bending model is developed to predict and design the deformability of any arbitrary 2D heterostructure.

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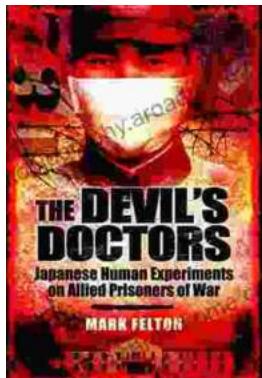
2D MATERIALS



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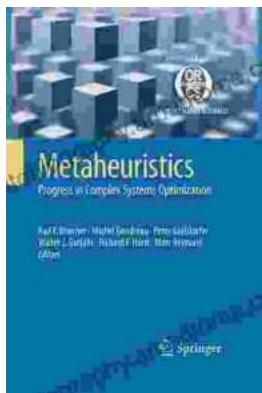
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